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# IOP

## Ethernet MCU with Hardwired TCP/IP Core

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### Overview

IOP is the Internet Offload Processor, the one-chip solution that integrates MCU and Hardwired TCP/IP cores.

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### Product Family

- [W7500](#): ARM Cortex-M0, 128KB Flash, Hardwired TCP/IP, 802.3 Ethernet MAC
  - [W7500P](#): ARM Cortex-M0, 128KB Flash, Hardwired TCP/IP, 802.3 Ethernet MAC w/ **PHY**
  - [W7100A](#): 80c51 compatible core, 64KB Flash, Hardwired TCP/IP, MAC w/ **PHY**
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### Etc.

#### Sidebar

- [iMCU sidebar](#)
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Last update: 2018/03/28 13:31

