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## WIZwiki Platform

- [WIZwiki-W7500](#)
- [W7500](#)
- [WIZwiki-W7500 mbed Starter Kit](#)
- [WIZwiki-W7500ECO](#)

The **IOP4IoT W7500** chip is the one-chip solution which integrates an ARM Cortex-M0, 128KB Flash and hardwired TCP/IP core for various embedded application platform especially internet of things.

## Hardwired TCP/IP Chip

- [W5500](#)

The W5500 chip is a Hardwired TCP/IP embedded Ethernet controller that enables easier internet connection for embedded systems using SPI (Serial Peripheral Interface). W5500 suits users in need of stable internet connectivity best, using a single chip to implement TCP/IP Stack, 10/100 Ethernet MAC and PHY. Hardwired TCP/IP stack supports TCP, UDP, IPv4, ICMP, ARP, IGMP, and PPPoE which has been proven through various applications over many years. W5500 uses a 32Kbytes internal buffer as its data communication memory.

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## 4-in-1 iMCU (Microcontroller + Hardwired TCP/IP)

- [W7100A \(under construction\)](#)
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## Evaluation Board

- [W5500-EVB](#): W5500 chip evaluation board
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## io Module (Internet Offload Module)

- [WIZ550io](#)
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## S2E Module (Serial to Ethernet Module)

- [WIZ107SR \(under construction\)](#)
  - [WIZ550S2E](#)
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## Application Module

- [WIZ550web](#): W5500 based Web Server module

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## Wi-Fi Module

- [WizFi250](#)
- [WizFi310](#)
- [WizFi630A](#)

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